

**10/526641**

**DT01 Rec'd PCT/PT 02 MAR 2005**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT: Francois Templier, et al.  
SERIAL NO.: unassigned CONFIRMATION No.: unassigned  
FILING DATE: herewith  
TITLE: Quasi-Vertical Power Semiconductor Device on a Composite Substrate  
EXAMINER: unassigned  
ART UNIT: unassigned

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COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria VA 22313-1450

**PRELIMINARY AMENDMENT**

Dear Sir:

Prior to examination, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 1 of this paper.

**Amendments to the Claims** are reflected in the listing of claims, which begins on page 2  
of this paper.

**Remarks / Arguments** begin on page 5 of this paper.